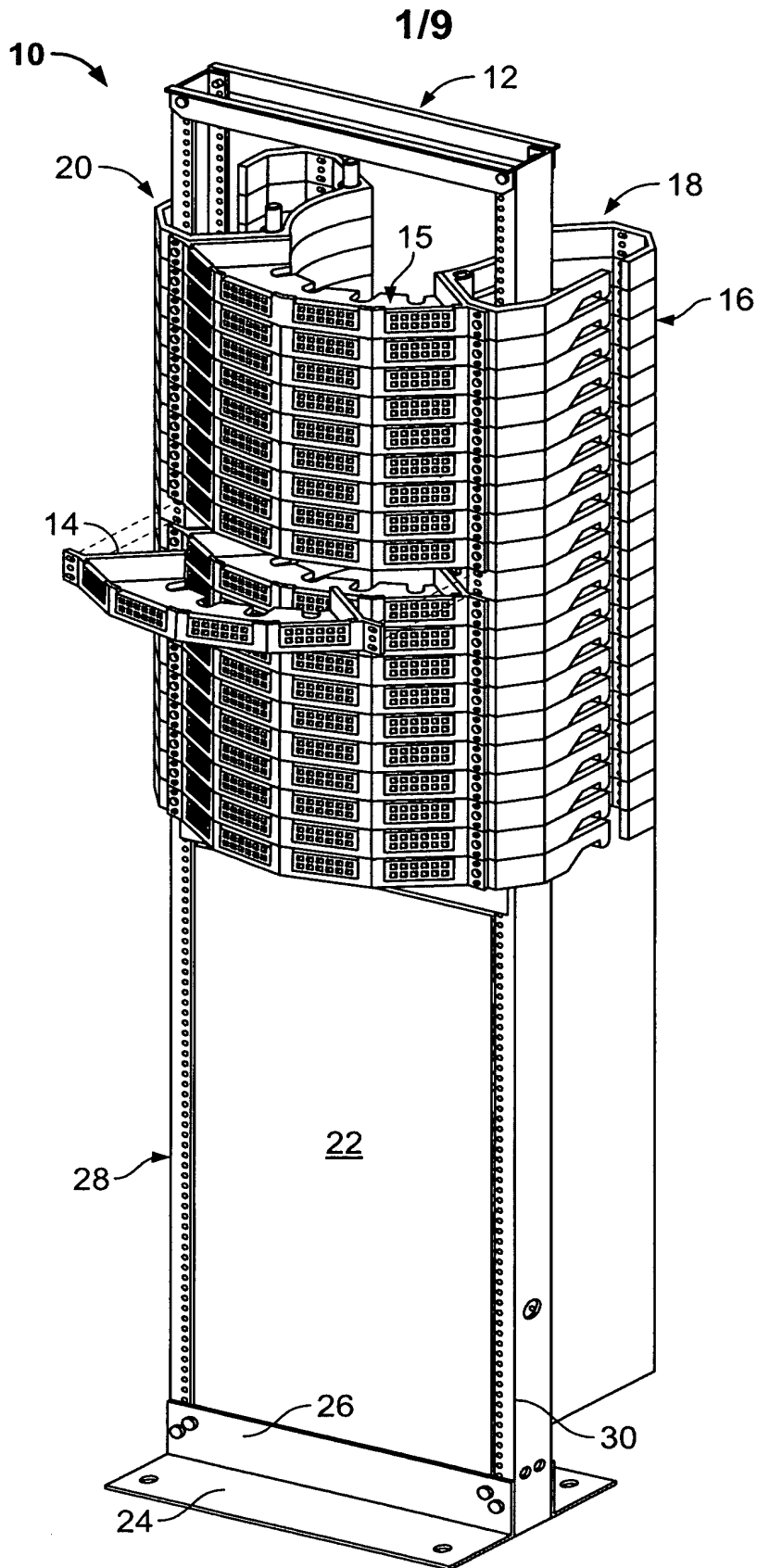




**TITLE: PATCH PANEL SYSTEM**  
Inventor: Denovich et al.  
S.N.: 10/823,937  
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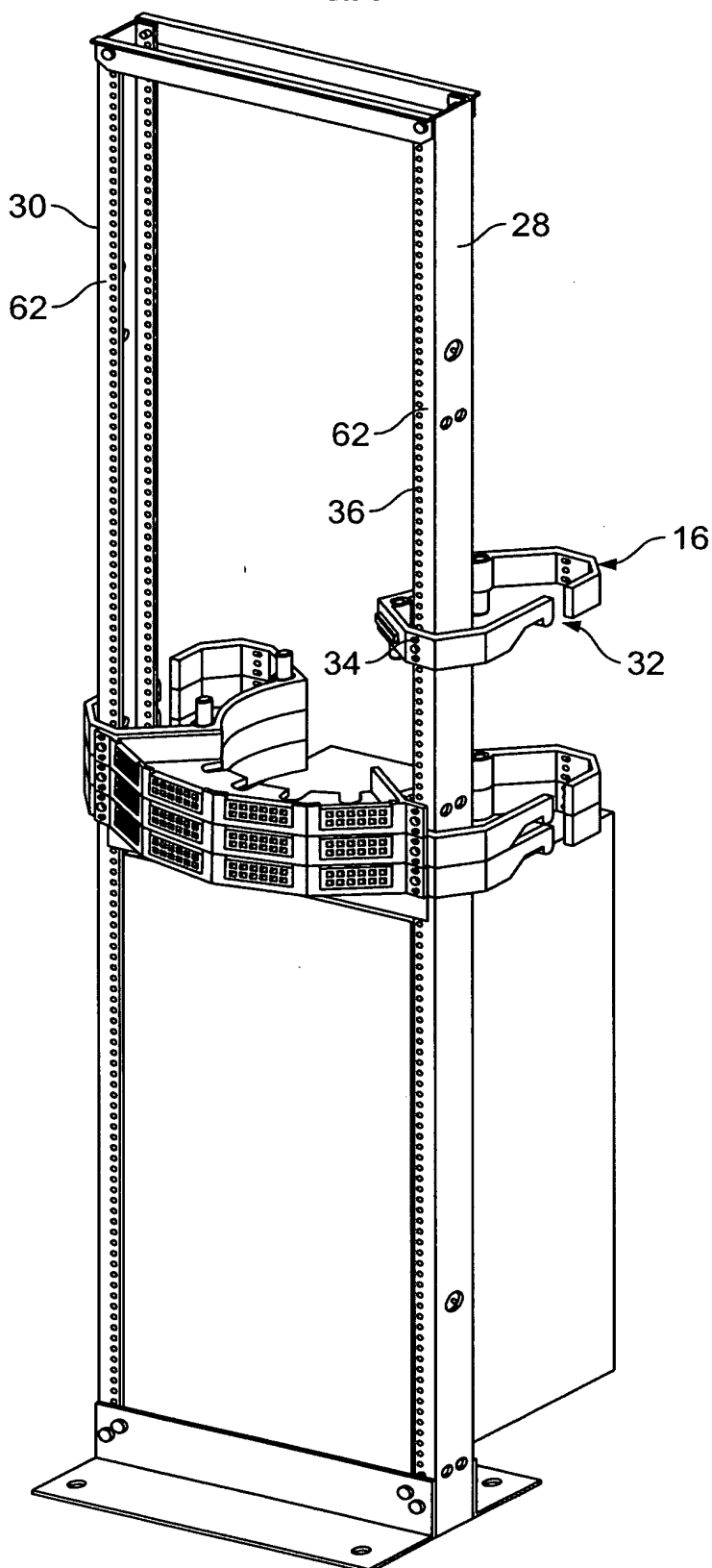


**FIG. 1**

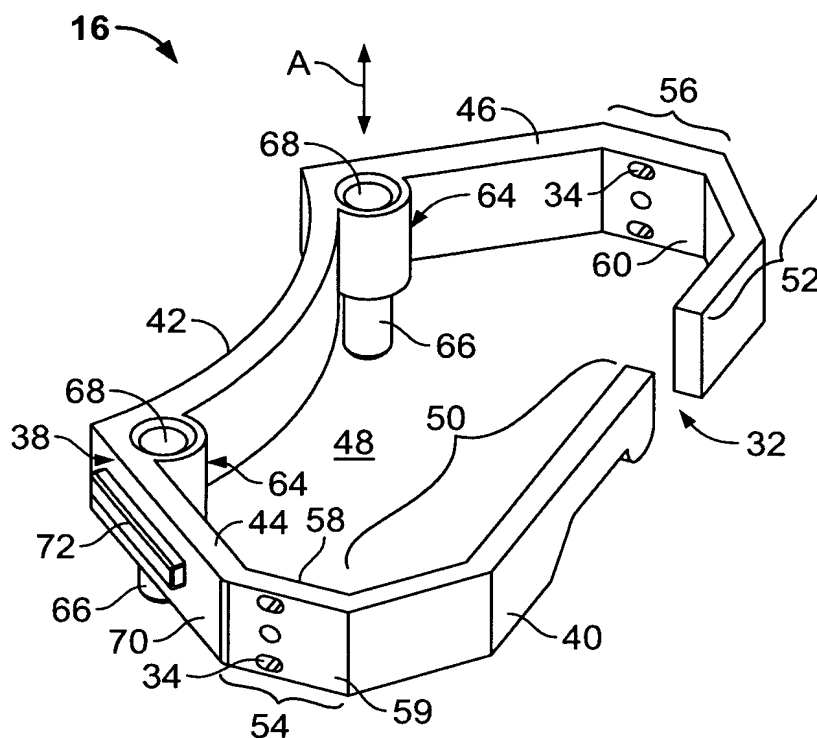
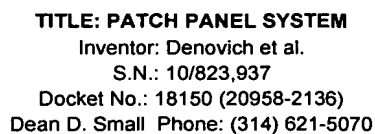


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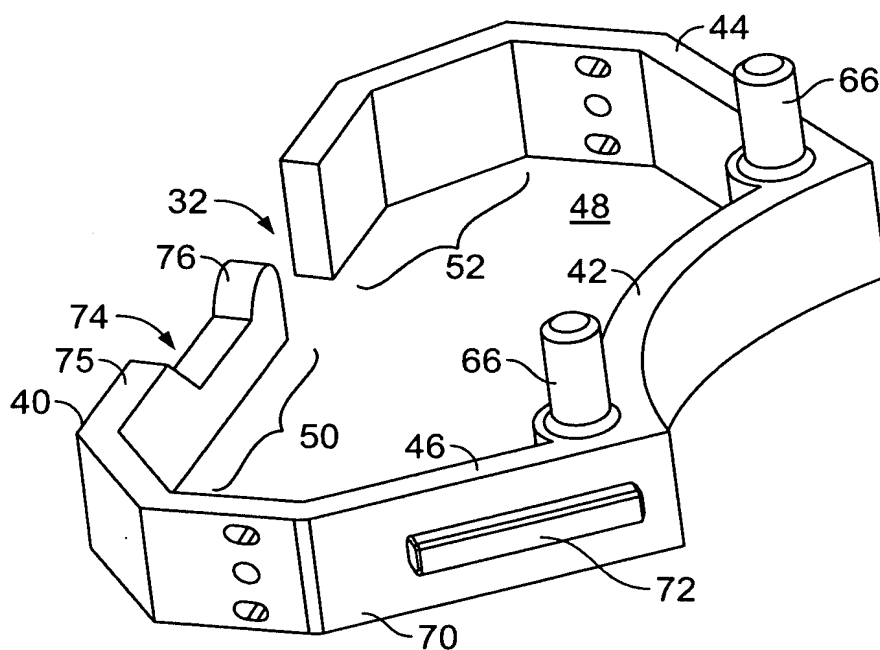
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**FIG. 2**



**FIG. 3**



**FIG. 4**



**TITLE: PATCH PANEL SYSTEM**

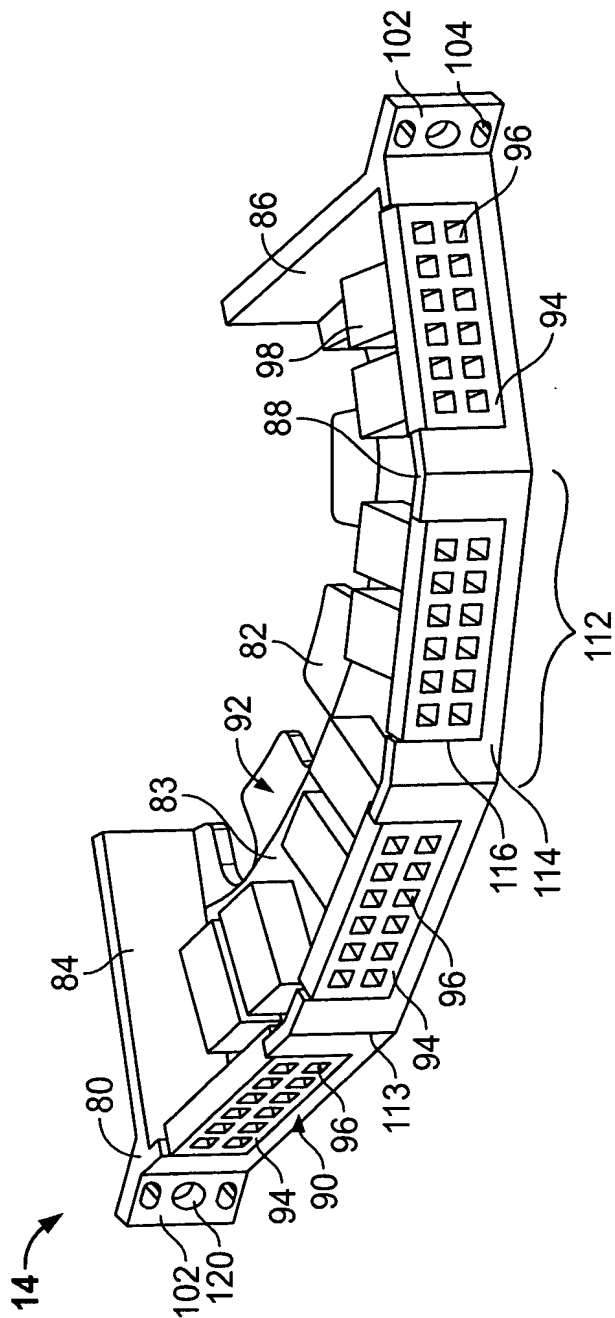
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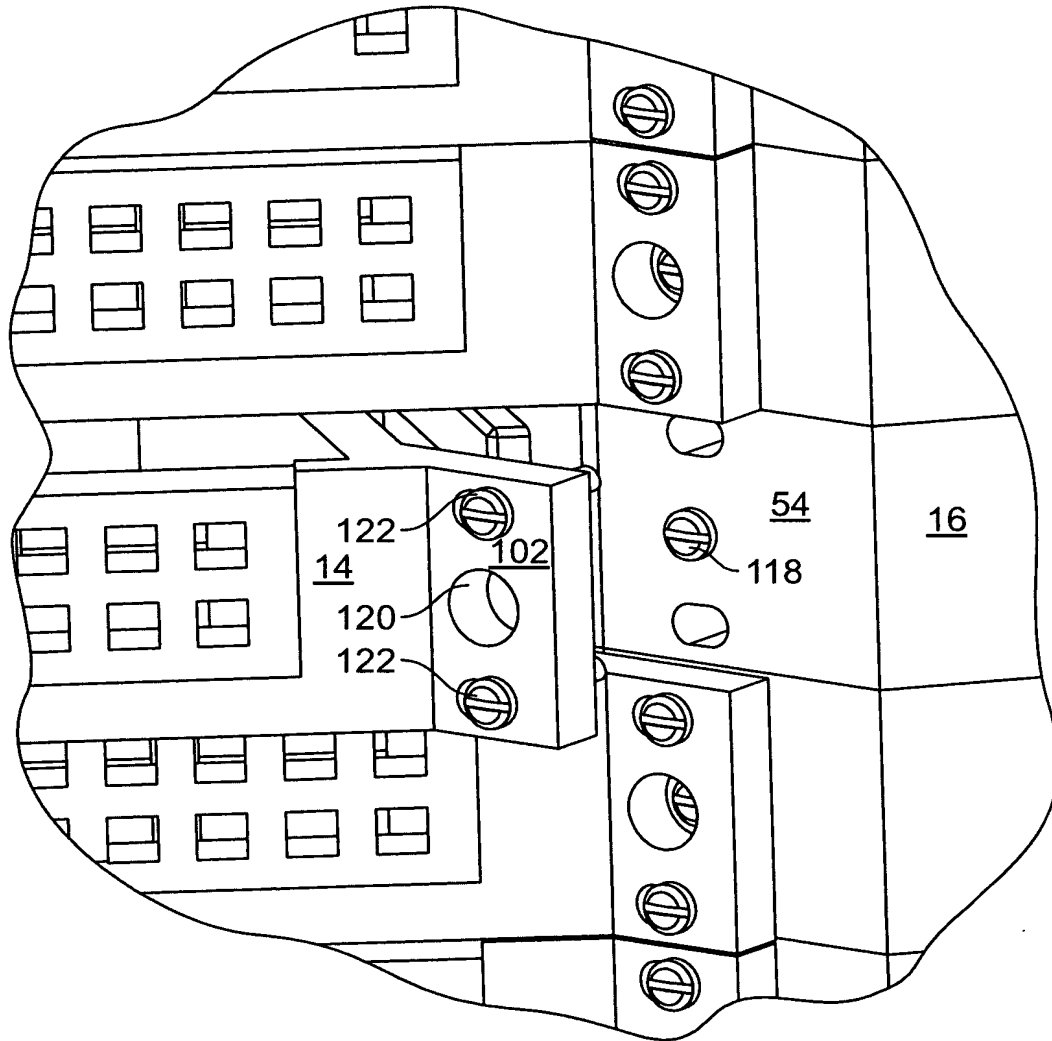
**FIG. 5**

FIG. 1 is a perspective view of a first embodiment of a multi-layer printed circuit board assembly. The assembly includes a main board 14 with a front panel 112, a top cover 113, and a base 102. Various components are mounted on the board, including connectors 94, 98, 100, and 106. A curved strip 82 is shown in a bent position, with arrows A, B, and C indicating its movement. Other components labeled include 83, 84, 86, 92, 108, and 110.

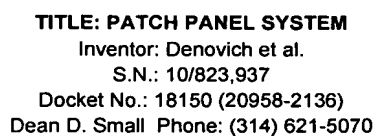


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**FIG. 7**



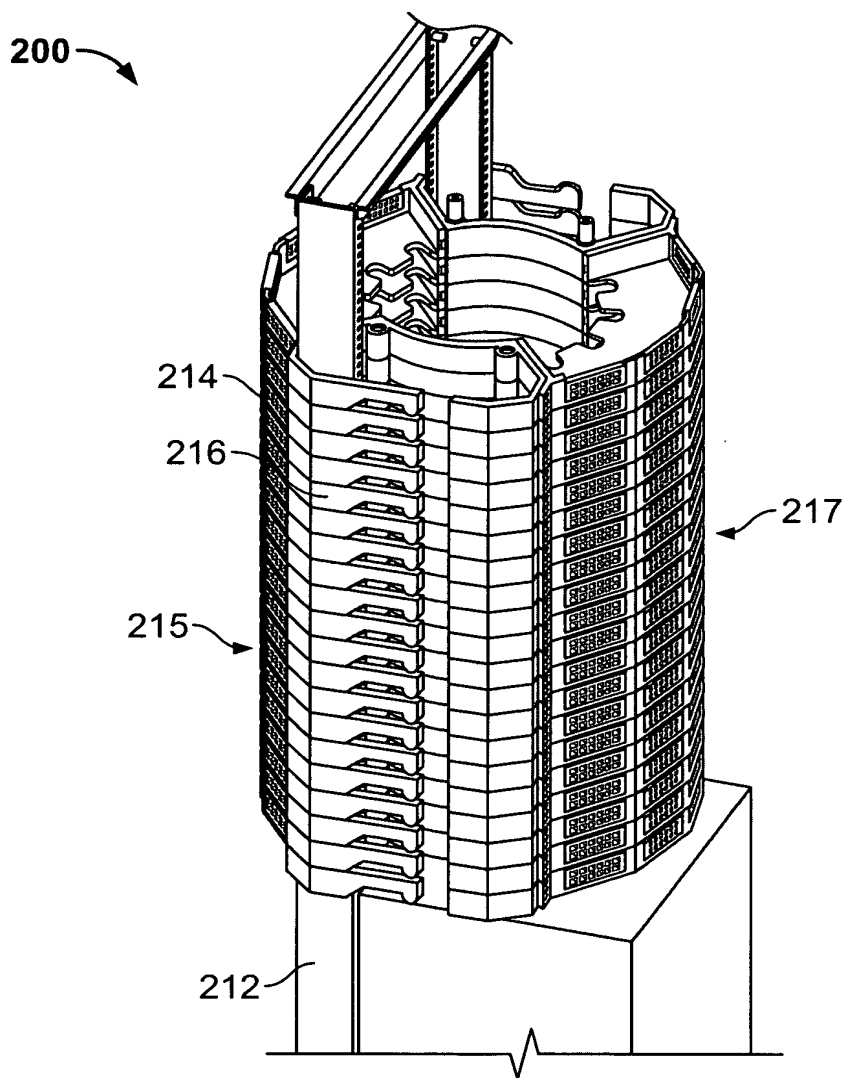
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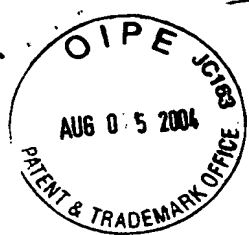
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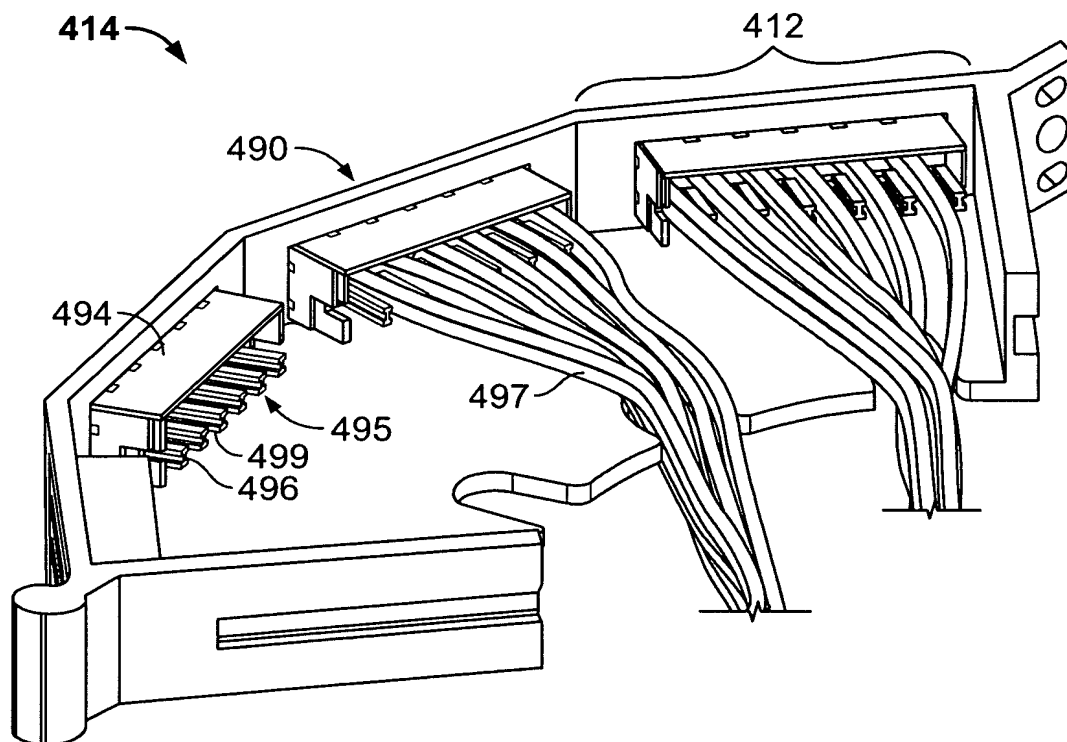
**FIG. 9**





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**FIG. 10**